



BEST AVAILABLE COPY

REMINDER

COPY

Applicant: Junichi YANO, et al. Docket No. 60188-126

Title: INTEGRATED CIRCUIT WITH CPU AND FPGA FOR RESERVED INSTRUCTIONS EXECUTION WITH CONFIGURATION DIAGNOSIS (As Amended)

Date Sent: 12/1/2004 ☒ Hand Carried ☐ Fax ☐ Electronic ☐ Cert. of Mailing ☐ First Class Mail ☐ Express Mail No. 10/001,967

☒ Transmittal Letter

☐ New Patent App ☐ Utility ☐ Design ☐ CIP ☐ Div. ☐ PCT ☐ RCE ☐ Prov

☐ Other: _____

☐ Small Entity ☐ Large Entity

☐ Declaration/Power of Attorney

☐ Recordation of Assignment/Security Agreement

☒ Information Disclosure Statement

☐ 1 Form PTO 1449

☐ 1 copies of cited references

☐ Preliminary Amendment

☐ Response to Missing Parts Notice

☐ Resp. to Notice to Correct App. Papers

☐ Certified Copy of Priority Doc.

☐ Claim for Convention Priority

☐ Response/Amendment to Office Action of _____

☐ Request for _____ month Extension of Time

☐ Letter submitting _____ pages of drawings

☐ Req. for Approval of Drawing Amendments

☐ Req. for Oral Hearing

☐ Not. of Appeal ☐ Appeal Brief ☐ Reply Brief

☐ Rule 312 Amendment/Letter

☐ Req. for Acknowledgement of Cited Art

☐ Issue Fee

☐ Publication Fee

☐ Req. for Certificate of Correction

☐ Maintenance Fee for _____ years after grant

☐ Fee Address Indication Form

☐ Terminal Disclaimer

☐ Petition to Commissioner

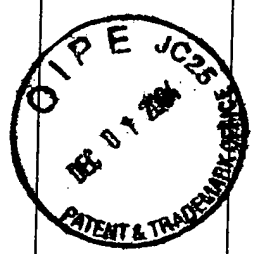
☐ Status Inquiry

☒ Other Copy of JP Office Action

Check for \$	<input checked="" type="checkbox"/> Charge Deposit Acct. 500417\$	180.00	Atty Init.	MEF	Trkr. #	3328	Secy. or PL:	TTGrollman
--------------	---	--------	------------	-----	---------	------	--------------	------------

CMS Descrip.: 10 - 180

THE PATENT AND TRADEMARK OFFICE DATE STAMPED HEREON IS ACKNOWLEDGEMENT THAT THE ITEMS, CHECKED ABOVE, WERE RECEIVED BY THE PTO ON THE DATE STAMPED.



Docket No.: 60188-126

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**REMINDER
COPY**

In re Application of

Junichi YANO, et al.

Application No.: 10/001,967

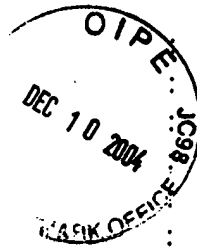
Filed: December 05, 2001

Customer Number: 20277

Confirmation Number: 1397

Group Art Unit: 2111

Examiner: Kim, Kenneth S.



For: INTEGRATED CIRCUIT WITH CPU AND FPGA FOR RESERVED
INSTRUCTIONS EXECUTION WITH CONFIGURATION DIAGNOSIS (As Amended)

INFORMATION DISCLOSURE STATEMENT

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee.

**REQUEST TO CONSIDER REFERENCES AFTER CLOSE OF PROSECUTION AND
BEFORE PAYMENT OF ISSUE FEE**

The undersigned hereby requests consideration and entry of this Information Disclosure Statement and accompanying references under 37 CFR 1.97(d).

Please charge the processing fee under 1.17(p) of \$180.00 to Deposit Account 500417.

CERTIFICATION PARAGRAPH

REMINDER
COPY

The undersigned certifies that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement as described in 37 CFR 1.97(e)(1).

The Japanese language reference, 2000-235489, was cited in a corresponding Japanese application office action and its relevance discussed therein. A copy of the foreign office action, together with an English language version thereof, is attached for the Examiner's information.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP


Michael E. Fogarty
Registration No. 36,139

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 MEF:tlg
Facsimile: 202.756.8087
Date: December 1, 2004

Please recognize our Customer No.
20277 as our correspondence address.



SHEET 1 OF 1

**INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION**

(PTO-1449)

ATTY. DOCKET NO.
60188-126SERIAL NO.
10/001,967**REMINDER**
COPYAPPLICANT
Junichi YANO, et al.FILING DATE
December 05, 2001GROUP
2111**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codez (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number + -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
		JP 2000-235489 A	8/29/2000	HITACHI LTD		w/English Abstract and partial English Translation

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

REMINDER
COPY

Applicant: Junichi YANO, et al.

Docket No. 60188-126

Serial/Reg./Patent No. 10/001,967

INTEGRATED CIRCUIT WITH CPU AND FPGA FOR RESERVED INSTRUCTIONS EXECUTION WITH
CONFIGURATION DIAGNOSIS (As Amended)

Title:

Date Sent: 9/20/2004

☒ Hand Carried

☐ Fax

☐ Electronic

☐ Cert. of Mailing

☐ First Class Mail

☐ Express Mail No.

☒ Transmittal Letter

New Patent App

☐ Utility

☐ Design

☐ Cont.

☐ CIP

☐ Div.

☐ PCT

☐ RCE

☐ Prov

☐ Other:

pages of Specification

pages of Claims

pages of Abstract

pages of Formal/Informal Drawings

☐ Small Entity

☐ Large Entity

☐ Declaration/Power of Attorney

☐ Recordation of Assignment/Security Agreement

☒ Information Disclosure Statement

Form PTO 1449

1 3 copies of cited references

☐ Preliminary Amendment

☐ Response to Missing Parts Notice

☐ Resp. to Notice to Correct App. Papers

☐ Certified Copy of Priority Doc.

☐ Claim for Convention Priority

☐ Response/Amendment to Office Action of

☐ Request for month Extension of Time

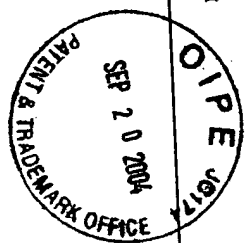
Check for \$ ☒ Charge Deposit Acct. 500417\$ 180.00

Att'y Init. MEF Tpr. # 3328

Secy. or Pl. TTGrollman

CMS Descrp.: 10 - 180

THE PATENT AND TRADEMARK OFFICE DATE STAMPED HEREON IS ACKNOWLEDGEMENT THAT THE ITEMS, CHECKED ABOVE, WERE RECEIVED BY THE PTO ON THE DATE STAMPED.



BEST AVAILABLE COPY

Docket No.: 60188-126



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
Junichi YANO, et al.	:	Confirmation Number: 1397
Serial No.: 10/001,967	:	Group Art Unit: 2111
Filed: December 05, 2001	:	Examiner: Kim, Kenneth S.
For: INTEGRATED CIRCUIT WITH CPU AND FPGA FOR RESERVED INSTRUCTIONS EXECUTION WITH CONFIGURATION DIAGNOSIS (As Amended)		

REMINDER

INFORMATION DISCLOSURE STATEMENT

Mail Stop. ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee.

**REQUEST TO CONSIDER REFERENCES AFTER CLOSE OF PROSECUTION AND
BEFORE PAYMENT OF ISSUE FEE**

The undersigned hereby requests consideration and entry of this Information Disclosure Statement and accompanying references under 37 CFR 1.97(d).

Please charge the processing fee under 1.17(p) of \$180.00 to Deposit Account 500417.

CERTIFICATION PARAGRAPH

The undersigned certifies that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement as described in 37 CFR 1.97(e)(1).

Each non-English language reference, listed on the attached PTO-1449, have been cited in a corresponding Japanese application office action and its relevance discussed therein. A copy of the foreign office action, together with an English language version thereof, is attached for the Examiner's information.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT WILL & EMERY LLP


Michael E. Fogarty
Registration No. 36,139

600 13th Street, N.W.
Washington, DC 20005-3096
202.756.8000 MEF:tlg
Facsimile: 202.756.8087
Date: September 20, 2004

SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

ATTY. DOCKET NO.
60188-126

SERIAL NO.
10/001,967

APPLICANT
Junichi YANO, et al.

FILING DATE
December 05, 2001

**GROUP
2111**

(PTO-1449)

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes -Number & -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
		WO 00/49496	8/24/2000	KONINKLIJKE PHILIPS ELECTRONICS N.V.		YES	
		JP 03-083136	4/9/1991	SEIKO EPSON CORP		w/English Abstract	
		JP 07-175631	7/14/1995	DAINIPPON PRINTING CO LTD		w/English Abstract	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.